

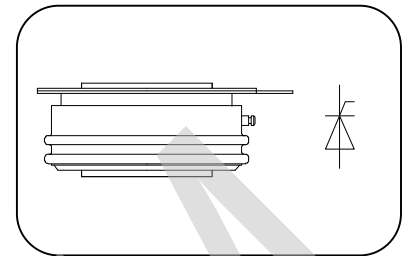
Features:

- n Interdigitated amplifying gates
- n Fast turn-on and high di/dt
- n Low switching losses

Typical Applications

- n Inductive heating
- n Electronic welders
- n Self-commutated inverters

$I_{T(AV)}$ 2472A
 V_{DRM}/V_{RRM} 1900~2500V
 t_q 35~70 μ s
 I_{TSM} 27KA
 I^2t 3645 10³A²S



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _J (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled, T _{hs} =55°C	125			2472	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	V_{DRM} & V_{RRM} , t _p =10ms V_{DSM} & V_{RSM} = V_{DRM} & V_{RRM} +100V	125	1900		2500	V
I_{DRM} I_{RRM}	Repetitive peak current	V_D = V_{DRM} V_R = V_{RRM}	125			200	mA
I_{TSM}	Surge on-state current	10ms half sine wave	125			27	KA
I^2t	I ² T for fusing coordination	V_R =0.6V _{RRM}	125			3645	A ² s*10 ³
V_{TO}	Threshold voltage		125			1.48	V
r_T	On-state slop resistance		125			0.18	mW
V_{TM}	Peak on-state voltage	I_{TM} =5000A, F=40KN	125			2.05	V
dv/dt	Critical rate of rise of off-state voltage	V_{DM} =0.67V _{DRM}	125			500	V/ μ s
di/dt	Critical rate of rise of on-state current	V_{DM} = 67%V _{DRM} , Gate pulse t _r ≤0.5 μ s I _{GM} =1.5A Repetitive	125			600	A/ μ s
I_{rm}	Reverse recovery current	I_{TM} =2000A, t _p =1000 μ s, di/dt=-20A/ μ s, V_R =50V	125		187		A
t_{rr}	Reverse recovery time		125		11.2		μ s
Q_{rr}	Recovery charge		125		1049	1200	μ C
t_q	Circuit commutated turn-off time	I_{TM} =2000A, t _p =1000 μ s, V_R =50V dv/dt=30V/ μ s, di/dt=-20A/ μ s	125	35		70	μ s
I_{GT}	Gate trigger current		25	40		450	mA
V_{GT}	Gate trigger voltage	V_A =12V, I _A =1A	25	0.9		4.5	V
I_H	Holding current		25	20		1000	mA
V_{GD}	Non-trigger gate voltage	V_{DM} =67%V _{DRM}	125	0.3			V
$R_{th(j-h)}$	Thermal resistance Junction to heat sink	At 180° sine double side cooled Clamping force 40KN				0.011	°C /W
F_m	Mounting force			35		47	KN
T_{stg}	Stored temperature			-40		140	°C
W_t	Weight				1100		g
Outline	KT73cT						

Outline:

